



2010-02

1

1

1

2

24

1

	x x							/				/	
	x x							1/4			6		
	x x							1/2			6		
	x x							1/4	FPGA		5		
	x x							0/0			6		

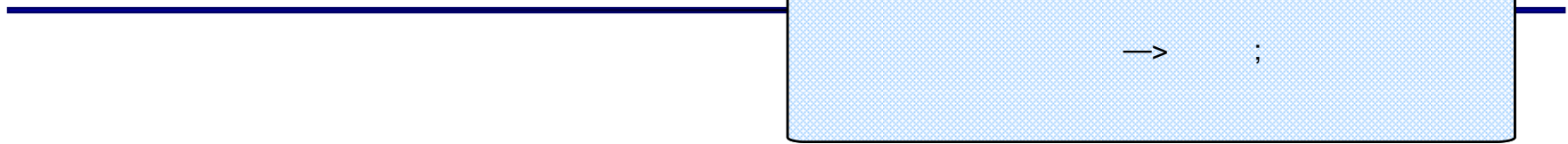


2-1

2-2



3



3

x x

2

x x

x x

190

x x mm

3

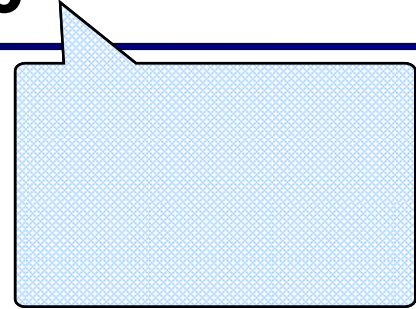
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2-3



6

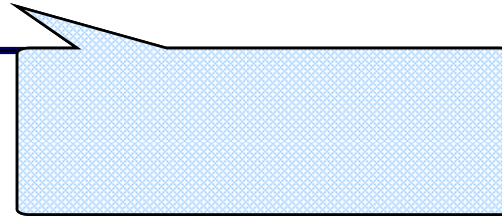


4

6

				/					
1		× ×		500000	1	500	12.0	12.0	3 4000 × 3 = 12 4000
							12.0	12.0	

2-4



5

x x

1			
2			
1			
2			
3			
4	80	80	$\begin{matrix} \times \times & & \times \times \\ \times \times & & 4800 \\ & & 40 \end{matrix} / \quad 40$ $/ \times 2 = 80$
3			
4			
5			
6			

3-1



4



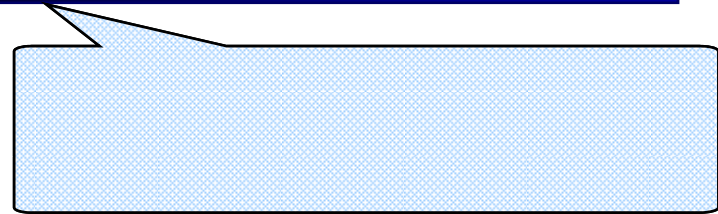
3-2



3-3



5



9

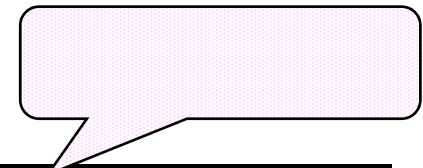
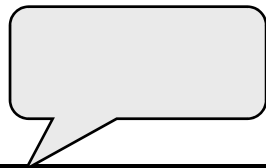
150

45

x x

5

195



1			5	x x	150.0	150.0	5 30 30 x 5 = 150
2			3		45.0	0.0	3 x x x x x x 15 x 3 = 45 15
					195.0	150.0	

3-4

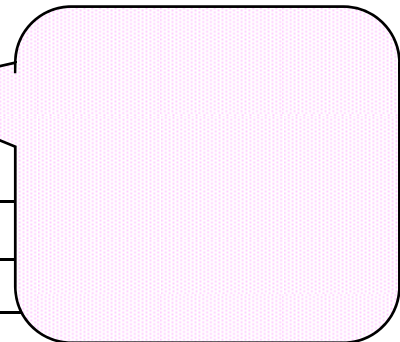


10

24.8

24.8

3				
1				
2				
3				
4 / /	24.8	24.8	1	$2500 / 8$ $2500 \times 8 = 20$ 2 $1000 + 800 = 1.8$ 3 $\times \times$
.....				



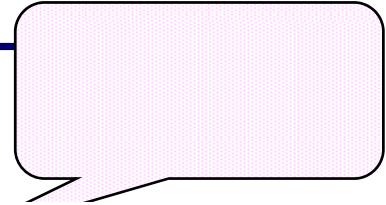
		900 /		2500 /
	/	500 /		
	PCT	30000		
			800	
				2000 /

3-5



11

34.525

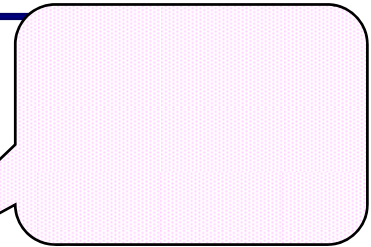


3-6

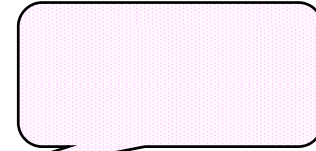


12

5670

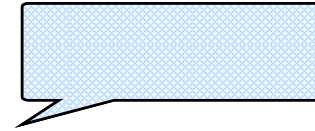


3			
1			
2			
3			
4 / /			
/			
5			
6	5.67	5.67	$ \begin{aligned} & 1 \quad 2000 / \quad 150 / \quad 50 / \quad 1 \quad 5 \quad 30 \\ & / \quad , \quad 1200 / \quad 2000+ \quad 150+50+30 \\ & \times 5+1200=4350 \\ & 2 \quad \times \times \quad 2 \quad 2 \quad 200 \\ & / \quad 150 / \quad 50 / \quad 30 / \quad . \\ & (200+(150+50+30) \times 2) \times 2=1320 \\ & 4350+1320=5670 \end{aligned} $



1							
2							
3							
4	250	250	1	30		5000	150
			2	40		2500 /	100
5	110	110	1				
				10			
			2		50		
			3			50	
6							

6-1



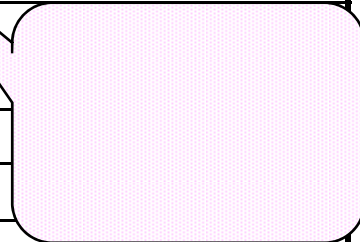
14

2

× ×

68.7

1				
2				
3				
4				
5				
6				
1	68.7	68.7	<p>1 10 × ×</p> <p>800 / / 7× 800 / / +10000</p> <p>+4400 =20000 20 / × 2 =40</p> <p>2 7-8 × × × ×</p> <p>10000 400 / 150 /</p> <p>500 [10000+ 400+150 × 7+500]× 2 =28700</p> <p>28.7+40=68.7</p>	



6-2



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