



2010-02

1

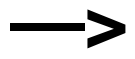
1

								/			/		
								1/4		6			
								1/2		6			
								1/4	FPGA	5			
								0/0		6			

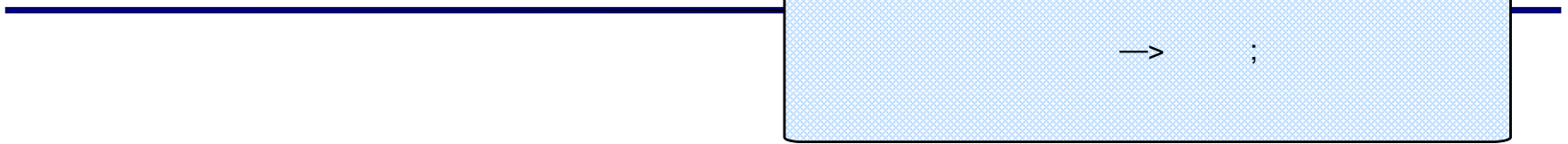


2-1

2-2



3



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2-3

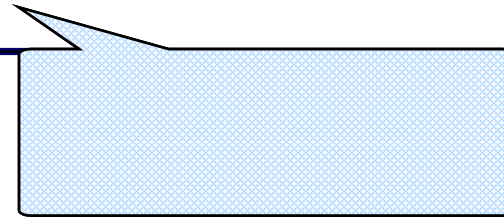


6



				/					
1				500000	1	500	12.0	12.0	3 4000 3=12 4000
							12.0	12.0	

2-4



1			
2			
1			
2			
3			
4	80	80	$\frac{4800}{40} / 40 = 80$
3			
4			
5			
6			

3-1



4

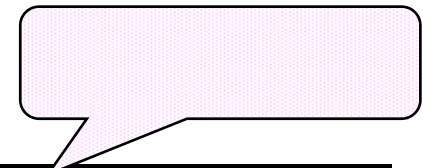
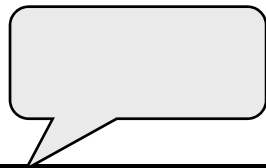
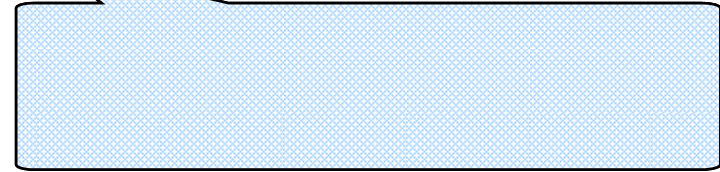




3-3



5

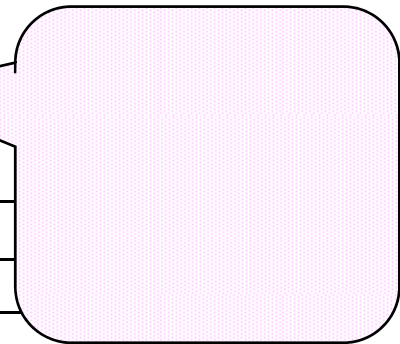


1			5		150.0	150.0	5 30 5=150 30
2			3		45.0	0.0	3 15 3=45 15
					195.0	150.0	

3-4

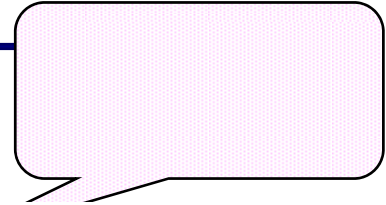


3									
1									
2									
3									
4 / /	24.8	24.8	1	6-10	2500 /	8			
				2500	8=20	2	1000 /		
				800 /	2		3		
					1000+800=1.8				
					3				
.....									

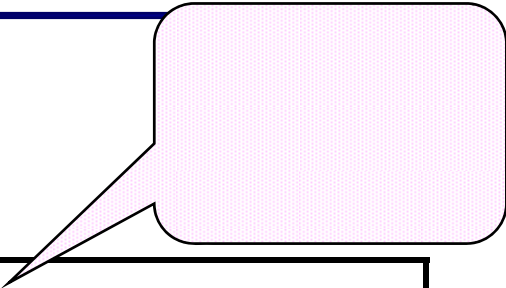


			2500 /	
	/			
	PCT	30000		
		800		
			2000 /	

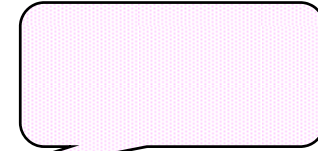
3-5



3-6

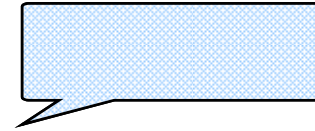


3			
1			
2			
3			
4 / /			
/			
5			
6	5.67	5.67	<p>1 2000 / 150 / 50 / 1 5 30</p> <p>/ , 1200 / 2000+ 150+50+30</p> <p>5+1200=4350</p> <p>2 150 / . 50 / . 2 2 30 / . 200</p> <p>(200+(150+50+30) 2) 2=1320</p> <p>4350+1320=5670</p>



1							
2							
3							
4	250	250	1	30		5000	150
			2	40		2500 /	100
5	110	110	1				
				10			
			2		50		
			3			50	
6							

6-1



1				
2				
3				
4				
5				
6				
1	68.7	68.7	1 10	$\frac{800}{20} + \frac{7 \times 800}{2} + 10000 + 4400 = 20000 + 2800 + 10000 + 4400 = 40000 + 2800 = 42800$ $\frac{10000}{2} + \frac{400}{7+500} + \frac{150}{2} = 5000 + 400 + 75 = 5575$ $28.7 + 40 = 68.7$

6-2



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